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L# Hits	ts Search String	Databases
L1 131 L2 78 L3 8	state space same (reduce\$5 and model\$5) 8 state space same (reduce\$5 and model\$5) 8 state space and (reduce\$5 same model\$5) and transitiv\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB USPAT; EPO; JPO; DERWENT; IBM_TDB USPAT; EPO; JPO; DERWENT; IBM_TDB
Results of search set L2:	sh set L2:	
US 3662343 A	CREDIT CARD AUTOMATIC CURRENCY DISPENSER System and methodology and adaptive linear model prodictive control based on	19720509 235/381
US 6826521 B1	rigorous, nonlinear process model	
US 6795795 B2 US 6789237 B1	Probabilistic map for a building Efficient model order reduction via multi-point moment matching	20040921 702/181 20040907 716/4
US 6778968 B1	Method and system for facilitating opportunistic transactions using auto-probes	
US 6751582 B1	Kalman filter state estimation for a manufacturing system Method and system for enhanced design validation through trace tailoring	20040629 700/108 20040615 703/14
US 6748170 B1	Method for optimizing a mostly optical network	
US 6/26939 BZ	Memod of circuit verification in digital design	20040427 716/5
	Method and system for reducing the computation tree to include only model	
US 6715107 B2	behaviors defined succinctly by a user via regular expressions as being of interest 20040330	
US 66910/3 B1	Adaptive state space signal separation, discrimination and recovery Time-memory tradeoff control in counteravameds production	20040210 702/190 20031316 716/5
US 6643827 B1	Symbolic model checking with dynamic model pruning	
	Surveillance system and method having parameter estimation and operating	
US 6609036 B1	mode partitioning	20030819 700/30
US 6587751 B2	Robot device and method for controlling the robot's emotions	20030701 700/245
US 6567959 B2		20030520 716/5
00 0544000	Signal and pattern detection or classification by estimation of continuous	
US 6508102 B1	uyilaniikal IIIodeis Near real-time friction estimation for pre-emptive vehicle control	20030513 /0Z/189 20030424 72/8
	System for combining plurality of input control policies to provide a compositional	
US 6473851 B1	output control policy	
US 6462962 B1	Lossless switching DC-to-DC converter	20021008 363/16

20020917 706/21 20020903 700/245 20020618 703/2 20020604 363/16 20020514 363/16 363/16 20020507 717/100	ns 20020219 703/2 20020129 717/136 20011120 703/15 20011120 84/738	20011113 701/13 20011016 363/16 20010925 703/13 20010911 700/97	20010327 20010123 20001212 20000815 20000808	20000411 19991221 19991109 19990921	19990831 703/13 19990720 310/90.5 19990608 702/190 19990504 703/6 19990309 700/97
Non-linear dynamic predictive device Robot device and method for controlling the same Method and an apparatus for analyzing a state based system model Lossless switching DC to DC converter with DC transformer Lossless switching converter with DC transformer Specification and verification for concurrent systems with graphical and textual editors Damping force control device and method	Method and system for modeling time-varying systems and non-linear systems Methods and apparatus for generating a verified algorithm for transforming a program from a first form to a second form Reduction of arbitrary L1-L2 circuits for enhanced verification System for enhancing the sound of an acoustic instrument Stable and verifiable state estimation methods and systems with spacecraft	Switching both computer-aided design of a product or process Verifying hardware in its software context and vice-versa	verifying nardware in its software context and vice-versa Protocol checking for concurrent systems Method and apparatus for modeling the dynamic effects on a gyroscope Verification tool computation reduction Method of implementing a linear discrete-time state-space servo control system on a fixed-point digital signal processor in a disc drive	System and method for model size reduction of an integrated circuit utilizing net invariants Fault-tolerant magnetic bearing control system architecture Phase shift accommodation in active vibration damping system Time-series trend estimating system and method using column-structured recurrent neural network	Method for detecting errors in models through restriction Fault-tolerant magnetic bearing control system architecture Prediction of chiller compressor motor overheating Method for detecting errors in models through restriction Method for computer-aided design of
US 6453308 B1 US 6445978 B1 US 6408262 B1 US 6400579 B2 US 6388896 B1 US 6385765 B1 US 6366841 B1	US 6349272 B1 US 6343372 B1 US 6321184 B1 US 6320113 B1	US 6317662 B1 US 6304460 B1 US 6295515 B1 US 6289255 B1 US 6209120 B1	US 6209120 B1 US 6178394 B1 US 6158267 A US 6102959 A US 6101058 A	US 6049662 A US 6005315 A US 5983168 A US 5956702 A	US 5946481 A US 5925957 A US 5911127 A US 5901073 A US 5880959 A

US 5671336 A US 5645260 A	Apparatus based on n-variable unlimited recurrent adjustable network Active piezo-electric vibration isolation and directional bracket	19970923 19970708	706/30 248/550
US 5615137 A	On-the-fly model checking with partial-order state space reduction Method and apparatus for integration of traffic measurement and gueueing	19970325	703/17
US 5583792 A		19961210	709/224
US 5517594 A	Thermal reactor optimization	19960514	392/416
US 5513299 A	Operation planning system for robot	19960430	700/255
US 5398546 A	Determination of drill bit rate of penetration from surface measurements	19950321	73/152.44
US 5394322 A	Self-tuning controller that extracts process model characteristics	19950228	700/37
US 5325468 A	Operation planning system for robot	19940628	700/262
	Method and apparatus for controlling or processing operations of varying		
US 5057992 A	characteristics	19911015	700/52
US 5014220 A	Reliability model generator	19910507	706/45
	ON-THE-FLY MODEL OF SYSTEM FOR EXECUTING CHECKING THROUGH		
JP 07334566 A	THE USE OF REDUCED PARTIAL ORDER STATE SPACE	19951222	
JP 07141004 A	OPTIMUM FORRESEEN STUDY CONTROLLER	19950602	
JP 01084482 A	RETRIEVING DEVICE FOR INFORMATION TRACK	19890329	
EP 1221663 A2	A method of circuit verification in digital design	20020710	
EP 913781 A2	Static partial order reduction	19990506	
	METHOD OF IMPLEMENTING A LINEAR DISCRETE-TIME STATE-SPACE		
	SERVO CONTROL SYSTEM ON A FIXED-POINT DIGITAL SIGNAL		
WO 9908268 A1	PROCESSOR IN A DISC DRIVE	19990218	
EP 685792 A1	Model checking with state space reduction.	19951206	
NNRD42093	A Method to Optimize Environment Models in Formal Verification	19990401	
NNRD40776	An Iterative Method to Reduce State Space for Efficient Formal Verification	19980301	
	Optimistic Parallel Simulation of Continuous Time Markov Chains Using		
NN9108177	Uniformization.	19910801	
	Method for verifying complete model of design system such as integrated circuits, involves restoring subset of abstract variables of abstract model to produce		
US 20040123254 A	intermediate model using which complete model is verified	20040624	
	Generating reduced state space representation for model in compositional state system by converting representations into labelled transition system		
CA 2355974 A	representation	20030224	
	Method for optimising environment models in formal verification of computer designs - using state bits of design under test to control input behavioural		
RD 420093 A GB 2342766 B		19990410 20020109	

State space reducing iterative method for efficient formal verification - by analysing exact copy of model to remove logic from original after function is redefined, removing model and re-defining inputs as function of remaining model signals

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